

3572 U.S. PTO
10/083454

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|----------|-------------|-------|----------|------|-----------|
| 10083454 | 02/25/2002 | 257 | 650 | 2844 | LUAN THAI |

**APPLICANTS: Peterson Kenneth; Watson Robert;
2827

**CONTINUING DATA VERIFIED:
THIS APPLICATION IS A CIP OF 09/571.335 05/16/2000 PAT 6,384,473

** FOREIGN APPLICATIONS VERIFIED:

| | | |
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| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed | <input type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO |
| 35 USC 119 conditions met | <input type="checkbox"/> yes <input type="checkbox"/> no | SD-7121 |
| Verified and Acknowledged Examiners's initials | | |
| TITLE : Single level microelectronic device package with an integral window | | |
| U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94) | | |

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|--|-----------|----------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| Assistant Examiner | | | |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drawg. | Figs. Drawg. |
| | | Print Fig. 34 | |
| Primary Examiner | | Application Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | |
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